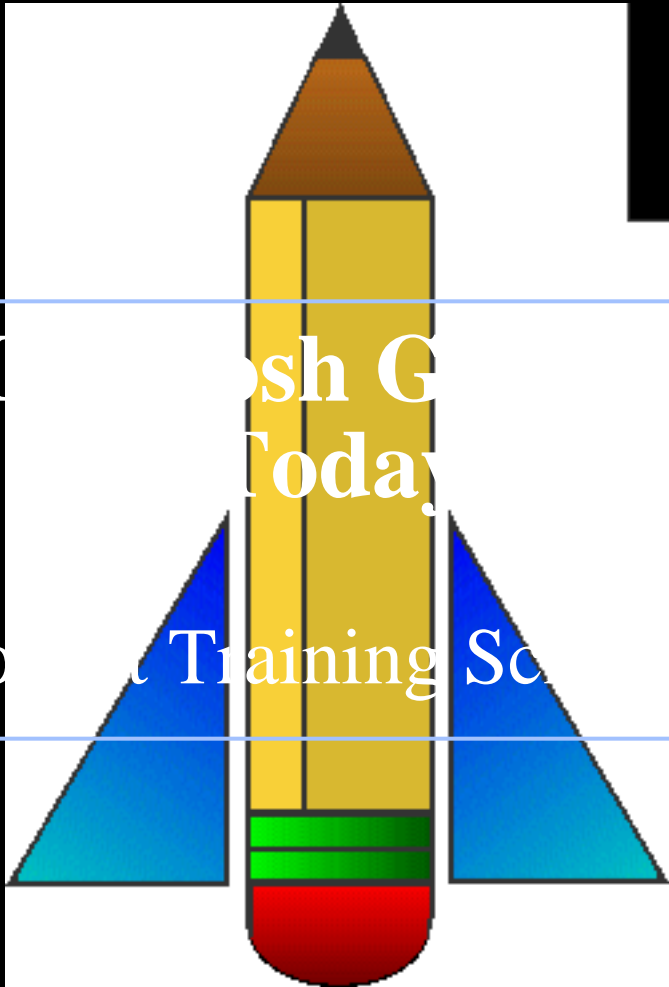




Jean-Charles Mourey

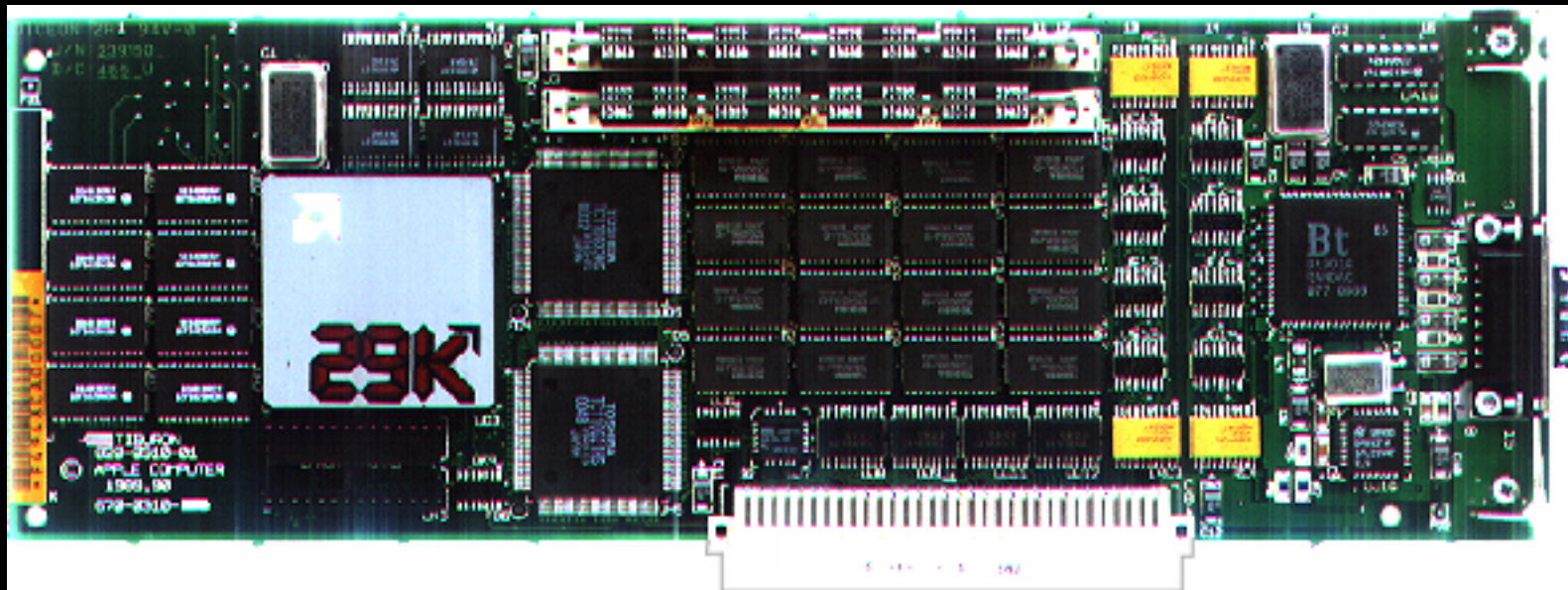
System Extension Group
“Chief Rocket Scientist”



Miss Gosh Goshes
Today

Rocket Training School

Macintosh Display Card 8•24 GC



Inside The 8•24 GC - What...

What It Does...

- Graphics accelerator and frame buffer in one



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- Will accelerate block transfer frame buffers



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- Will accelerate block transfer frame buffers
- Active even without monitor connected



Inside The 8•24 GC - What...

What It Does...

- Up to 24 bits per pixel on 640 x 480 displays



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 - 16 million colors
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What It Does...

- Up to 24 bits per pixel on 640 x 480 displays
- Up to 8 bits per pixel on larger displays
- Photo quality in color or grayscale
 - 16 million colors
 - 256 shades of gray
- NTSC and PAL timing support
 - Breakout box required for compositing



Inside The 8•24 GC - Hardware

How It Works...

- AM29000 RISC, 30MHz, 22 MIPS



Inside The 8•24 GC - Hardware

How It Works...

- AM29000 RISC, 30MHz, 22 MIPS
- NuBus master and slave block transfer



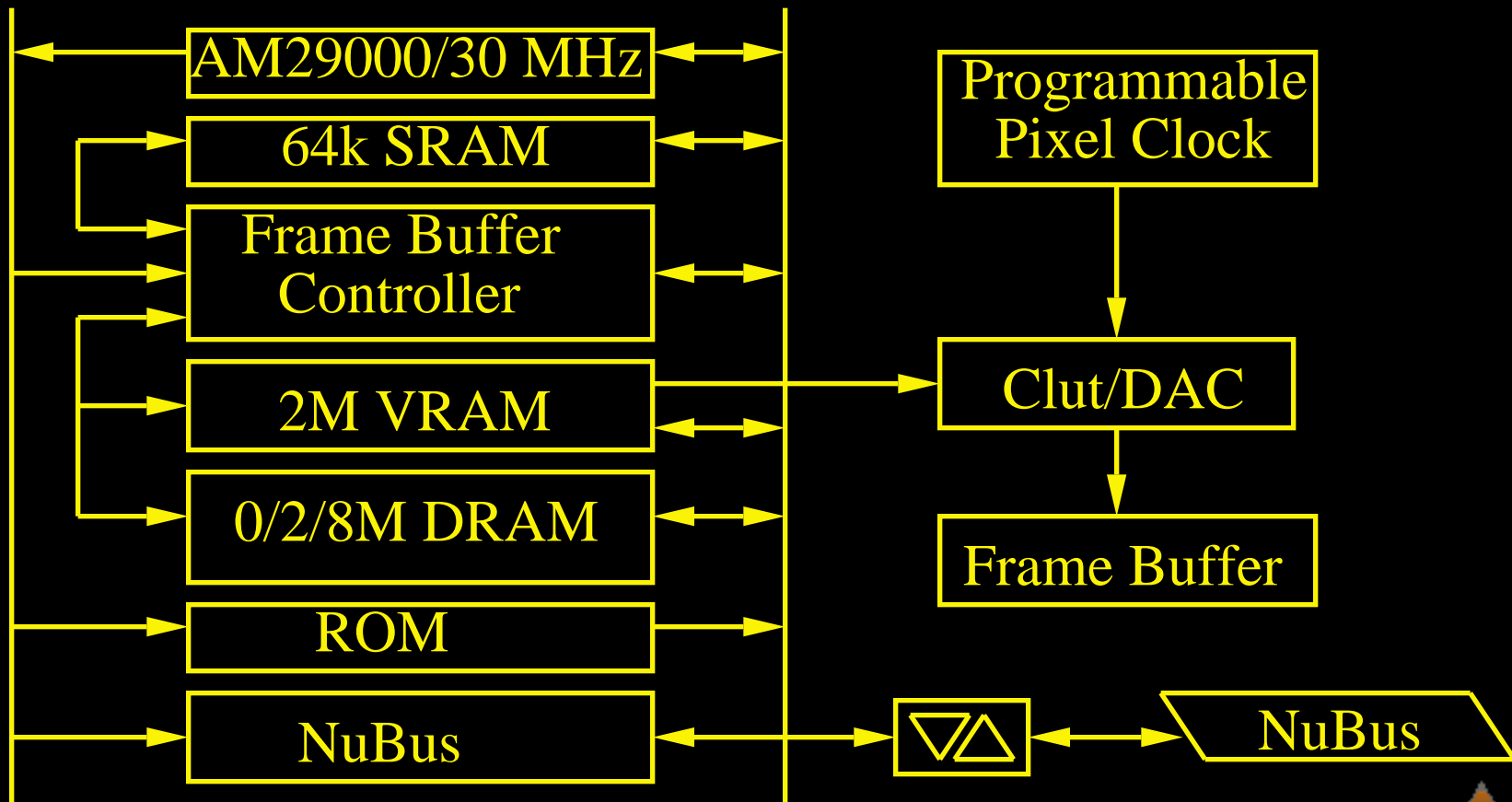
Inside The 8•24 GC - Hardware

How It Works...

- AM29000 RISC, 30MHz, 22 MIPS
- NuBus master and slave block transfer
- Pseudo block transfer



Block Diagram of the 8•24 GC Card



Inside The 8•24 GC - Software

“Magic” Components

- GC QuickDraw



Inside The 8•24 GC - Software

“Magic” Components

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- GC IPC



Inside The 8•24 GC - Software

“Magic” Components

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- GC OS



Inside The 8•24 GC - Software

How it Works...

- Loads with System 6.0.5



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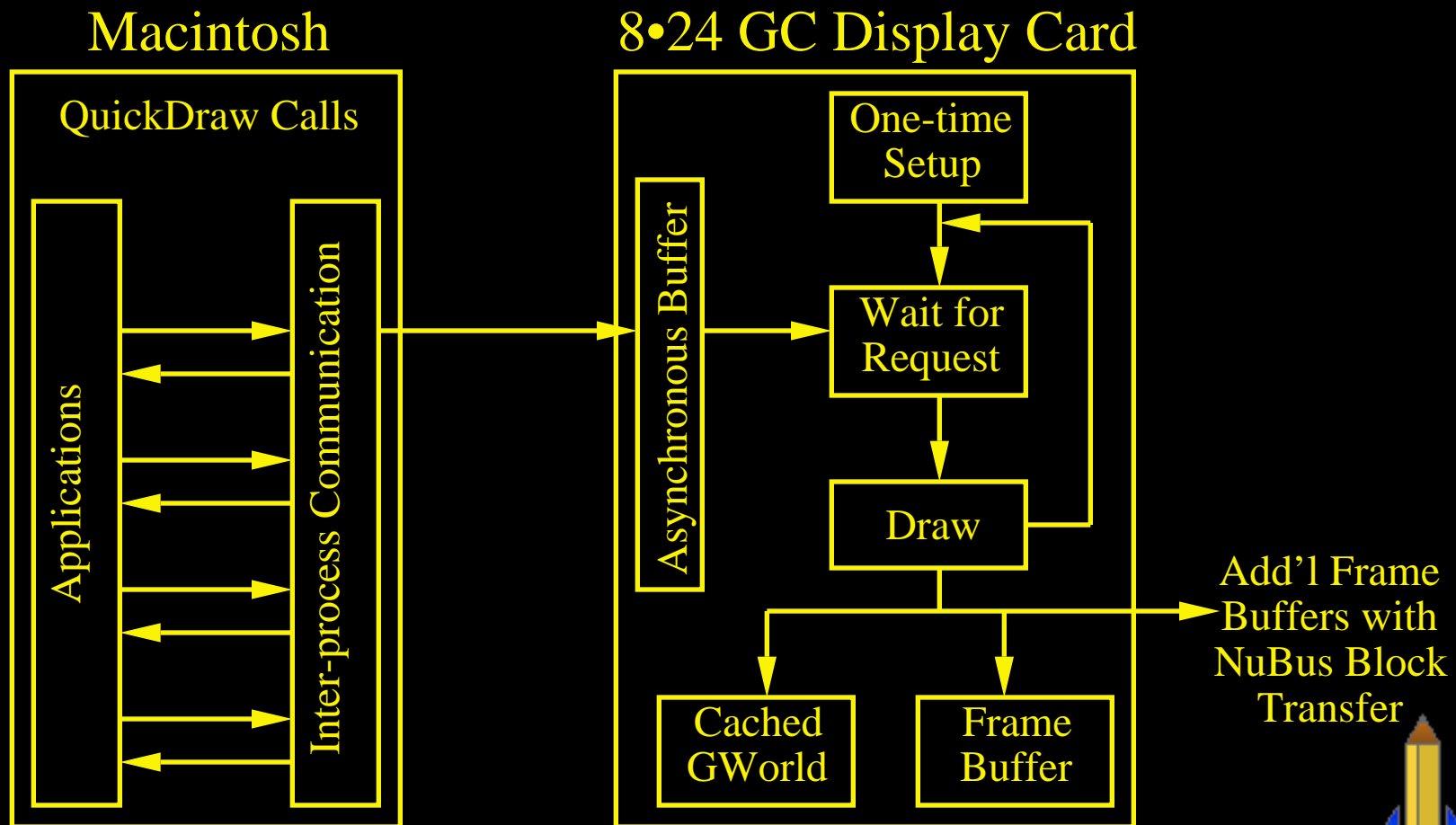
Inside The 8•24 GC - Software

How it Works...

- Loads with System 6.0.5
- Transparent upgrade to 32-Bit QuickDraw
- Designed on a “port” basis
- Eliminates the port set-up overhead
- Completely asynchronous - few exceptions
- Communicates via IPC and async queue



Software Architecture of 8•24 GC Card



Inside The 8•24 GC - Applications

How To Write Yours...

- Follow the guidelines



Inside The 8•24 GC - Applications

How To Write Yours...

- Follow the guidelines
- Don't change the structures directly



Inside The 8•24 GC - Applications

How To Write Yours...

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- Don't change the structures directly
- Don't draw directly to the screen



Inside The 8•24 GC - Applications

How To Write Yours...

- Follow the guidelines
- Don't change the structures directly
- Don't draw directly to the screen
 - You know who you are...



Inside The 8•24 GC - Applications

How To Write Yours...

- Follow the guidelines
- Don't change the structures directly
- Don't draw directly to the screen
- Don't rely on the speed of CPU for animation



Inside The 8•24 GC - Applications

How To Write Yours...

- Use PixPatChanged, CTabChanged, etc.



Inside The 8•24 GC - Applications

How To Write Yours...

- Use `PixPatChanged`, `CTabChanged`, etc.
- The GC software caches:
 - Color tables, `GDevices`, `GWorlds`
 - `PixPats`, fonts, width tables, and more...



Inside The 8•24 GC - Optimizing

How To Optimize...

- Avoid drawing to alternating ports



Inside The 8•24 GC - Optimizing

How To Optimize...

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- GWorlds ... GWorlds ... GWorlds ... GWorlds



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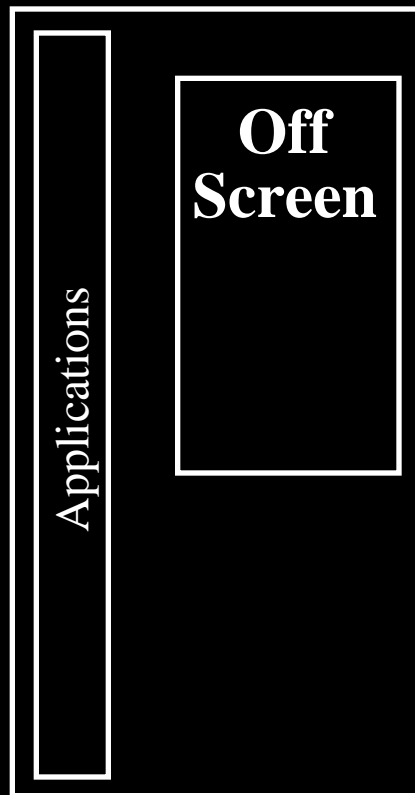
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- Offscreens: Old-Style and GWorlds

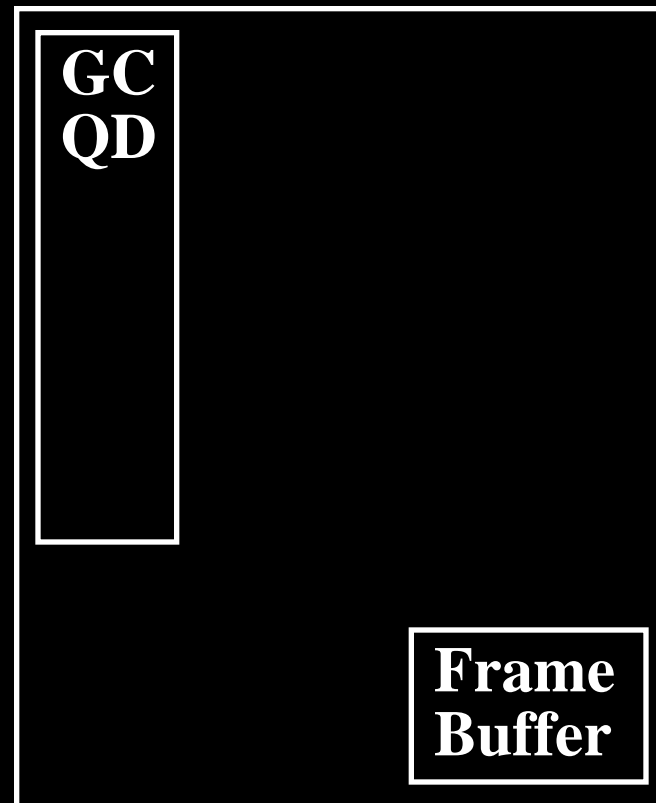


Offscreens: No Parallelism - Old-Style

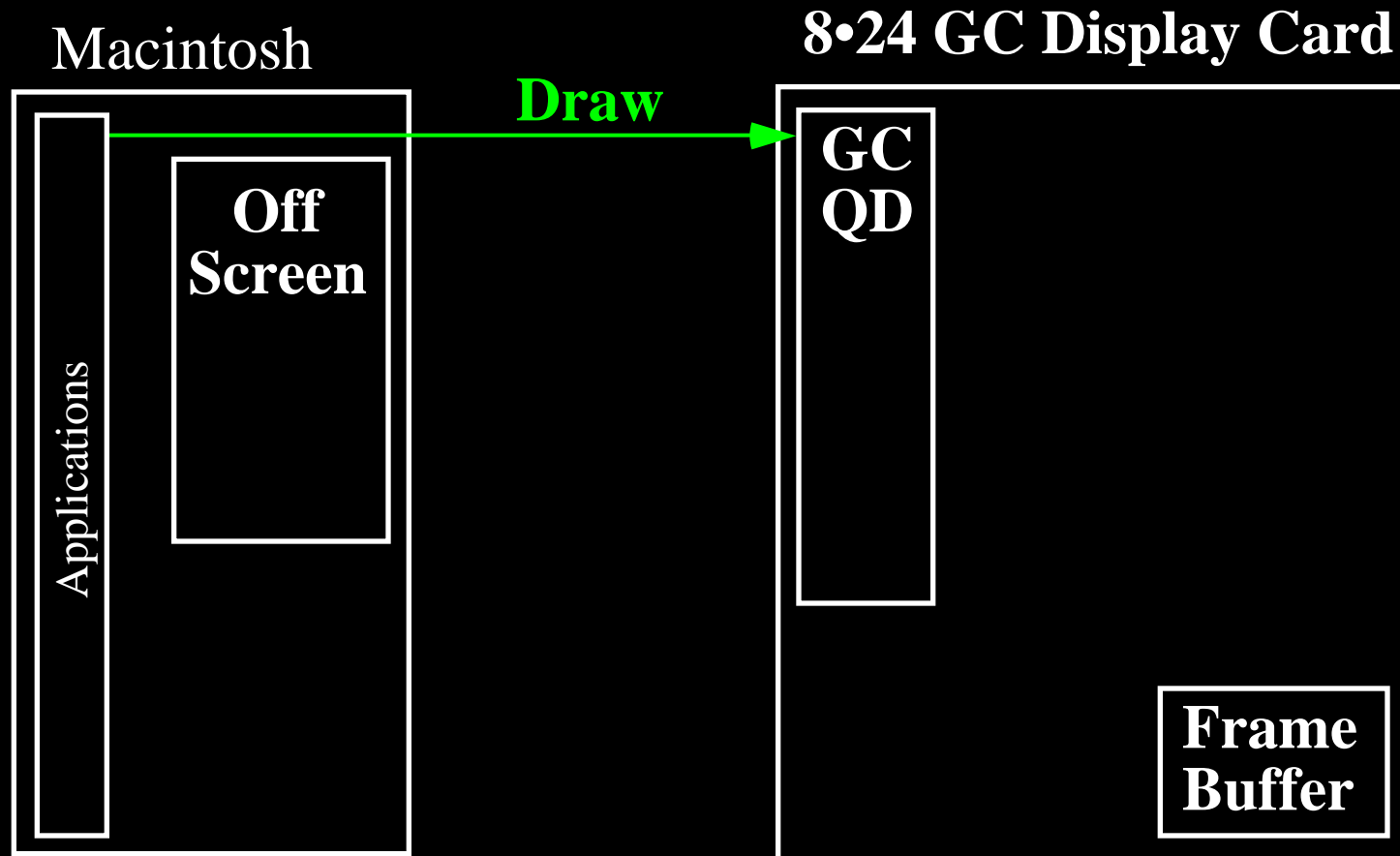
Macintosh



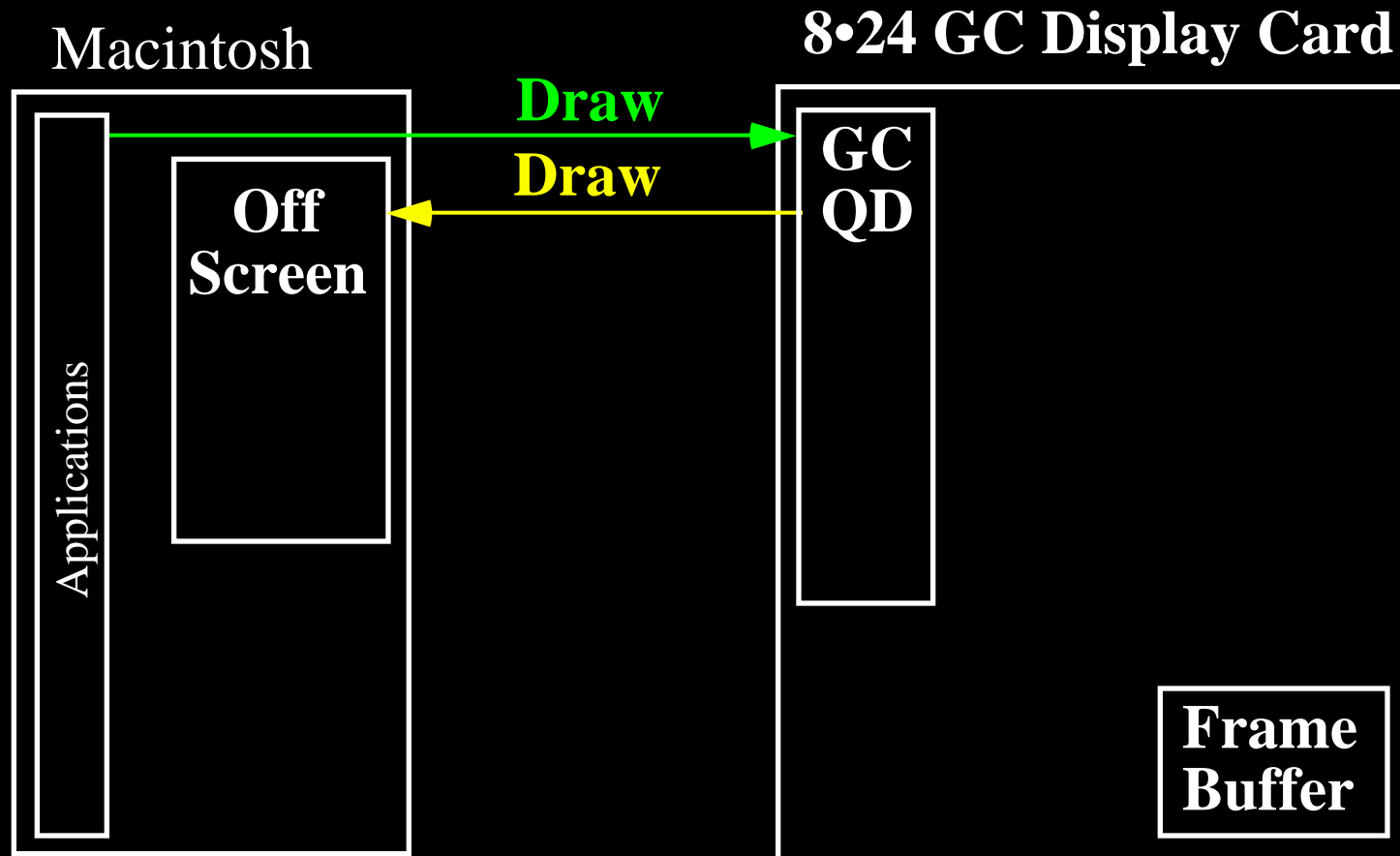
8•24 GC Display Card



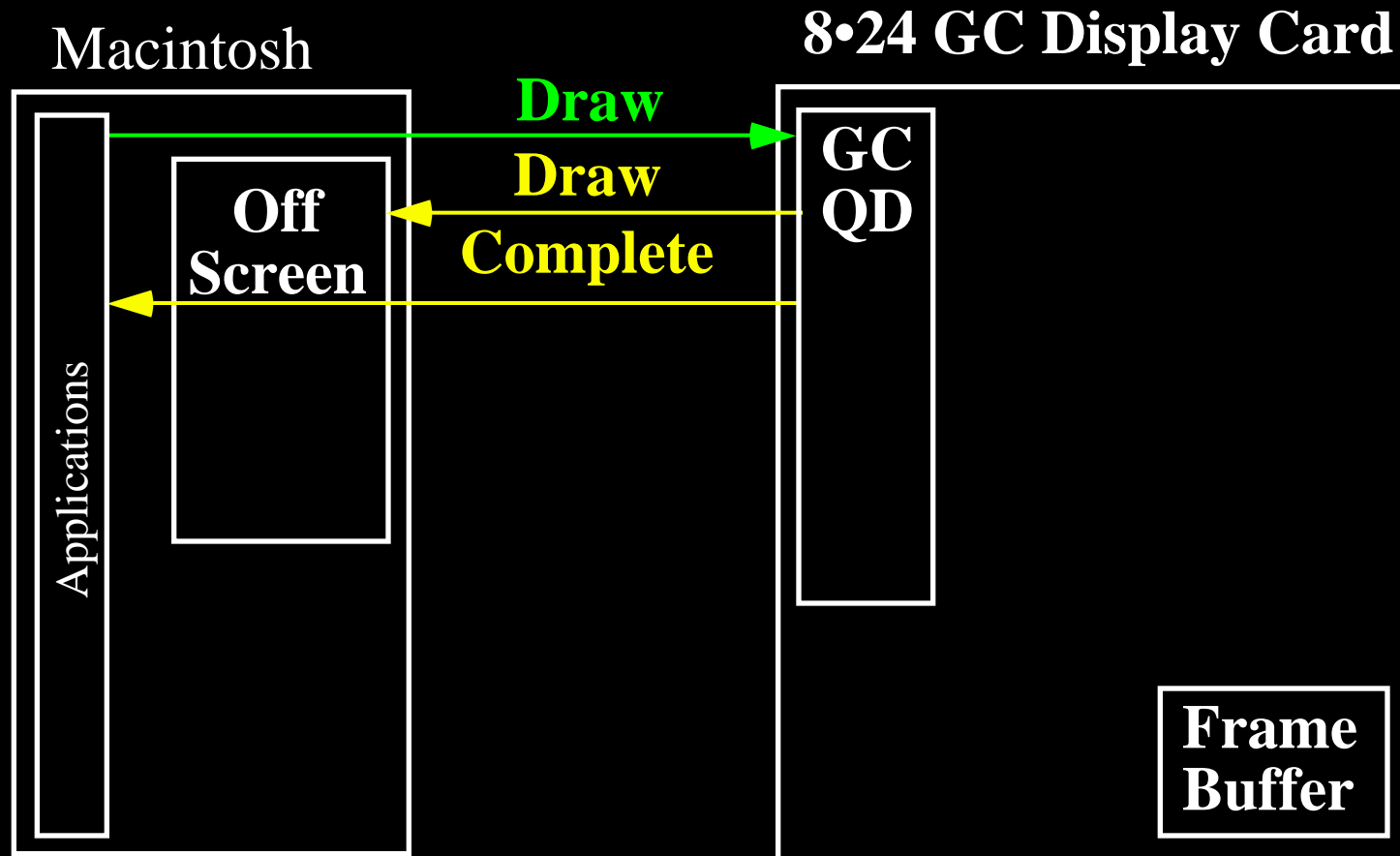
Offscreens: No Parallelism - Old-Style



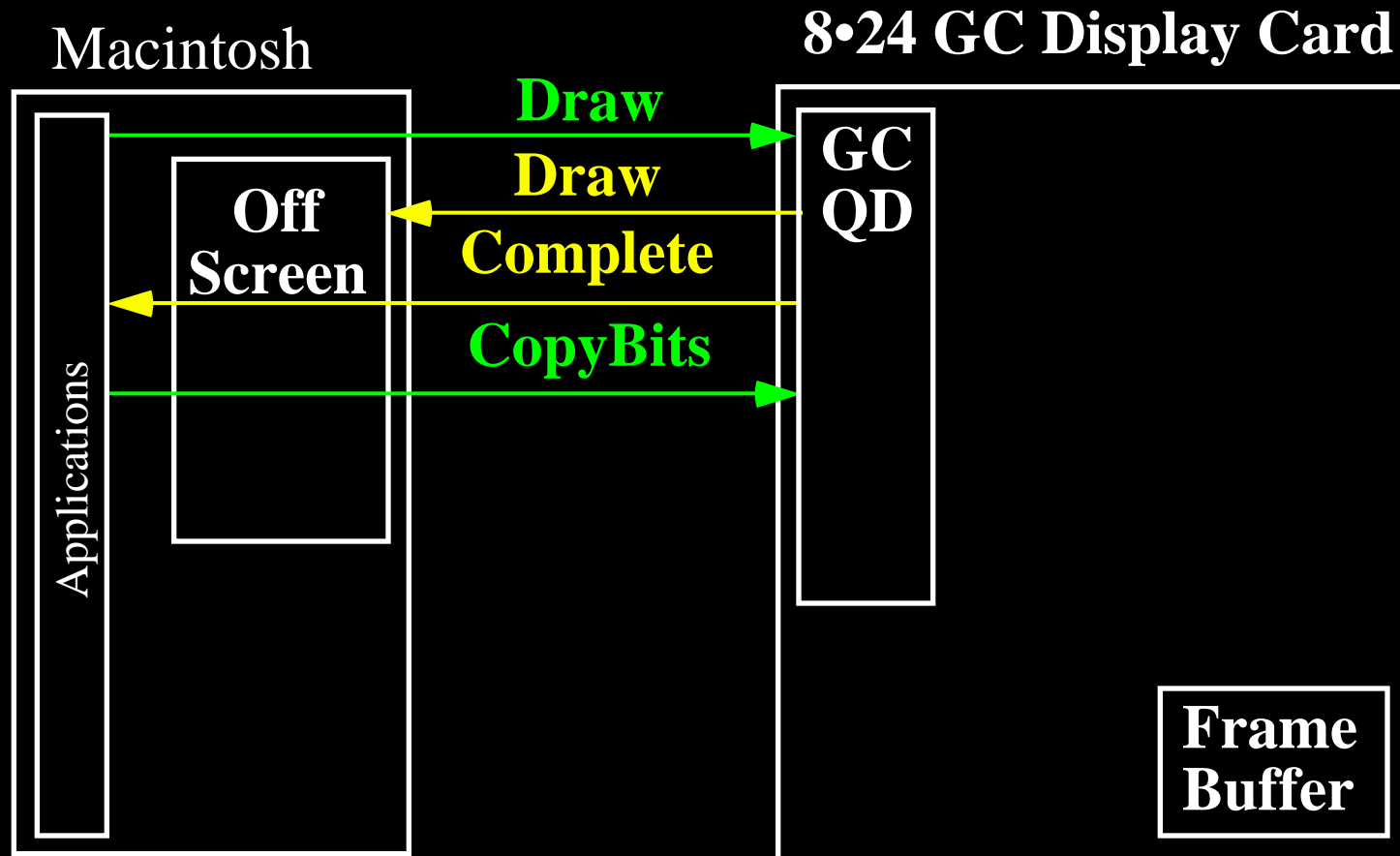
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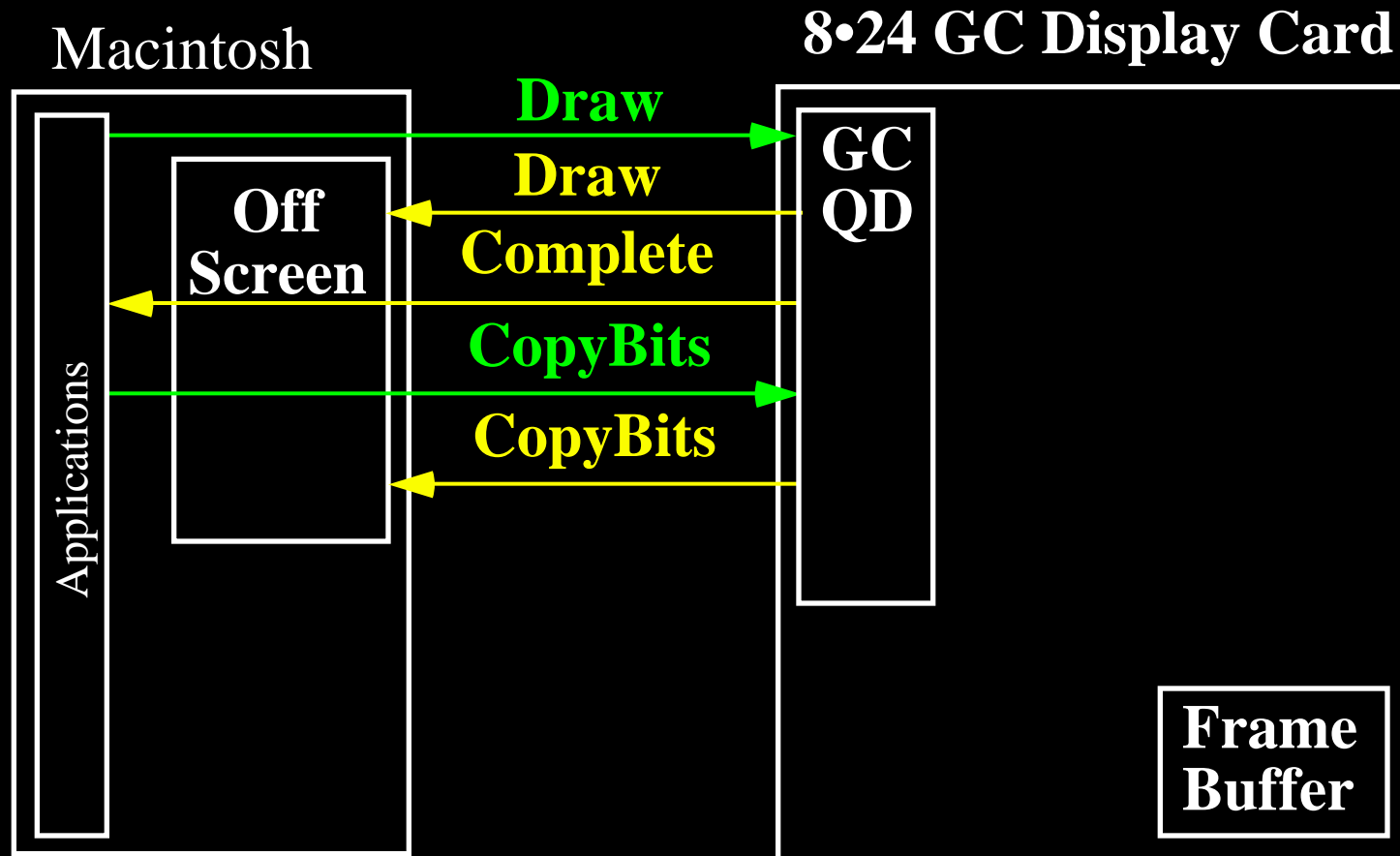
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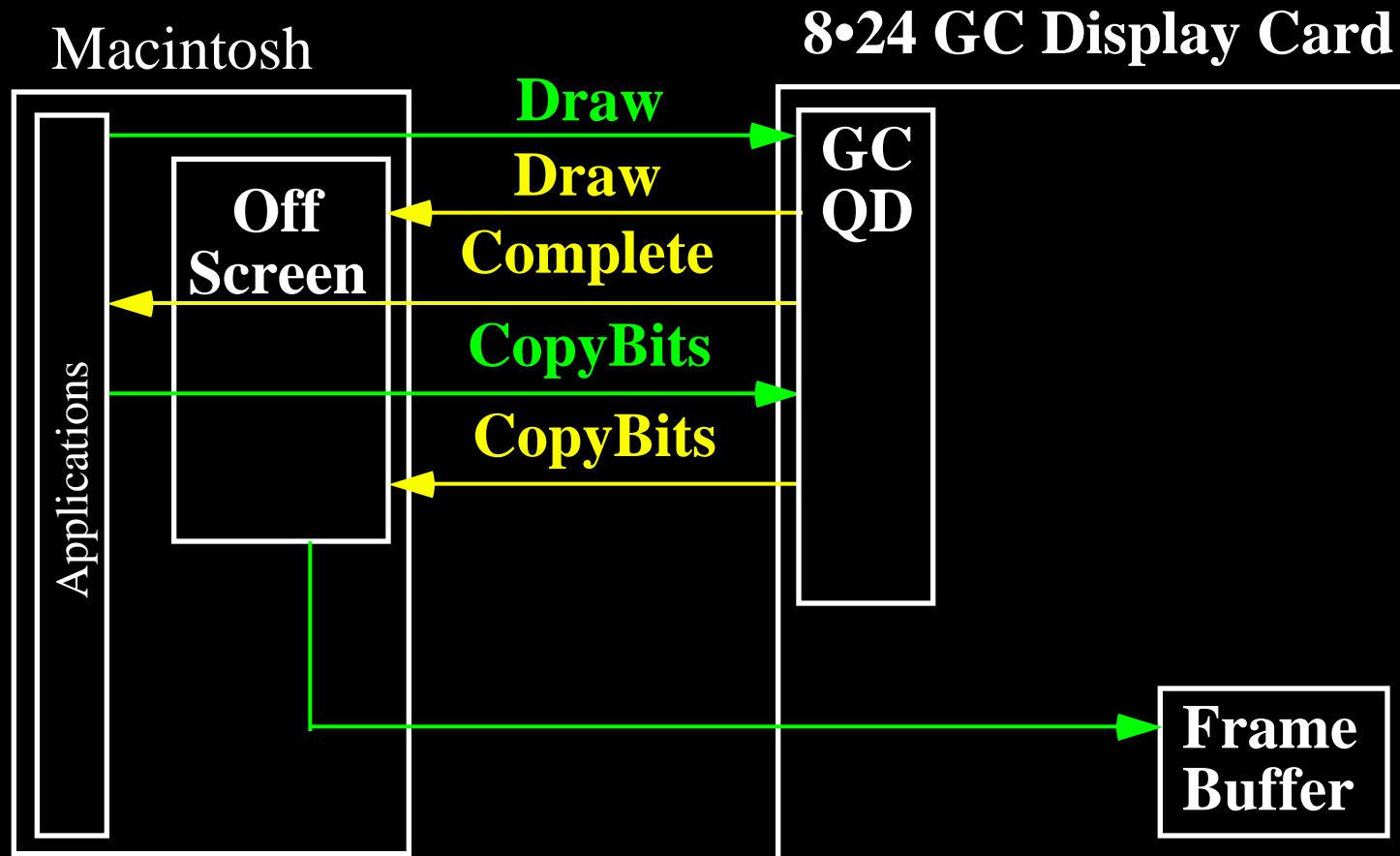
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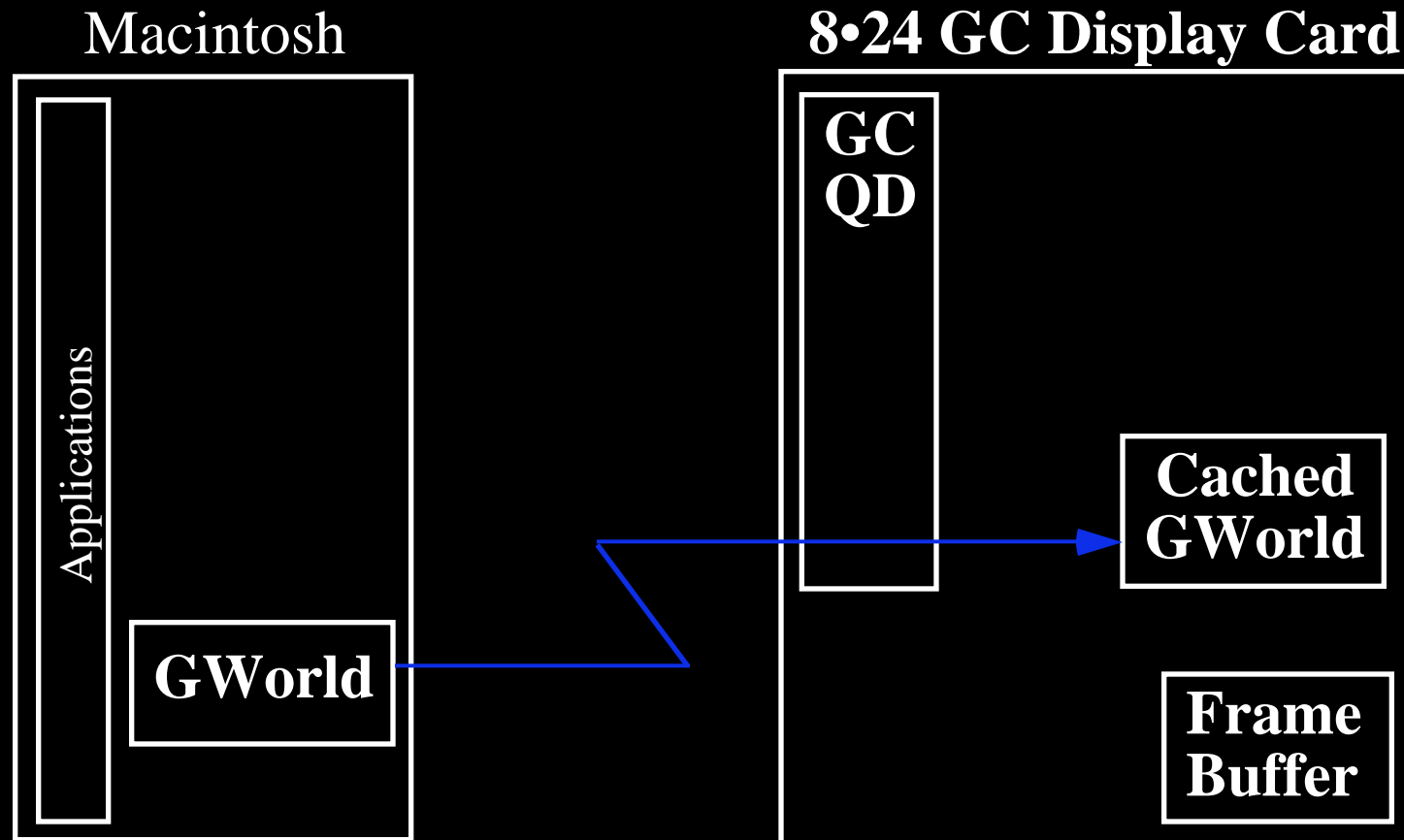
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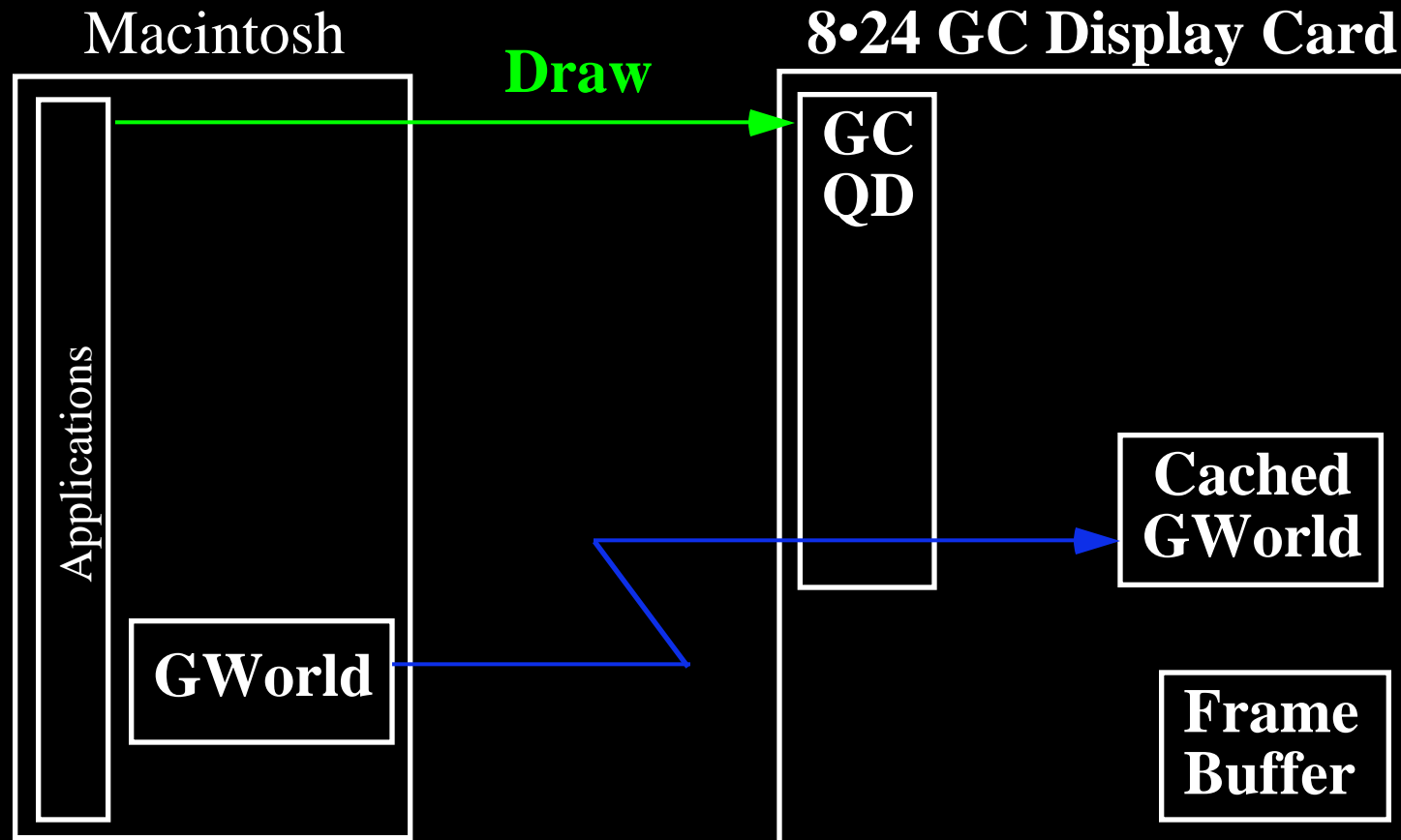
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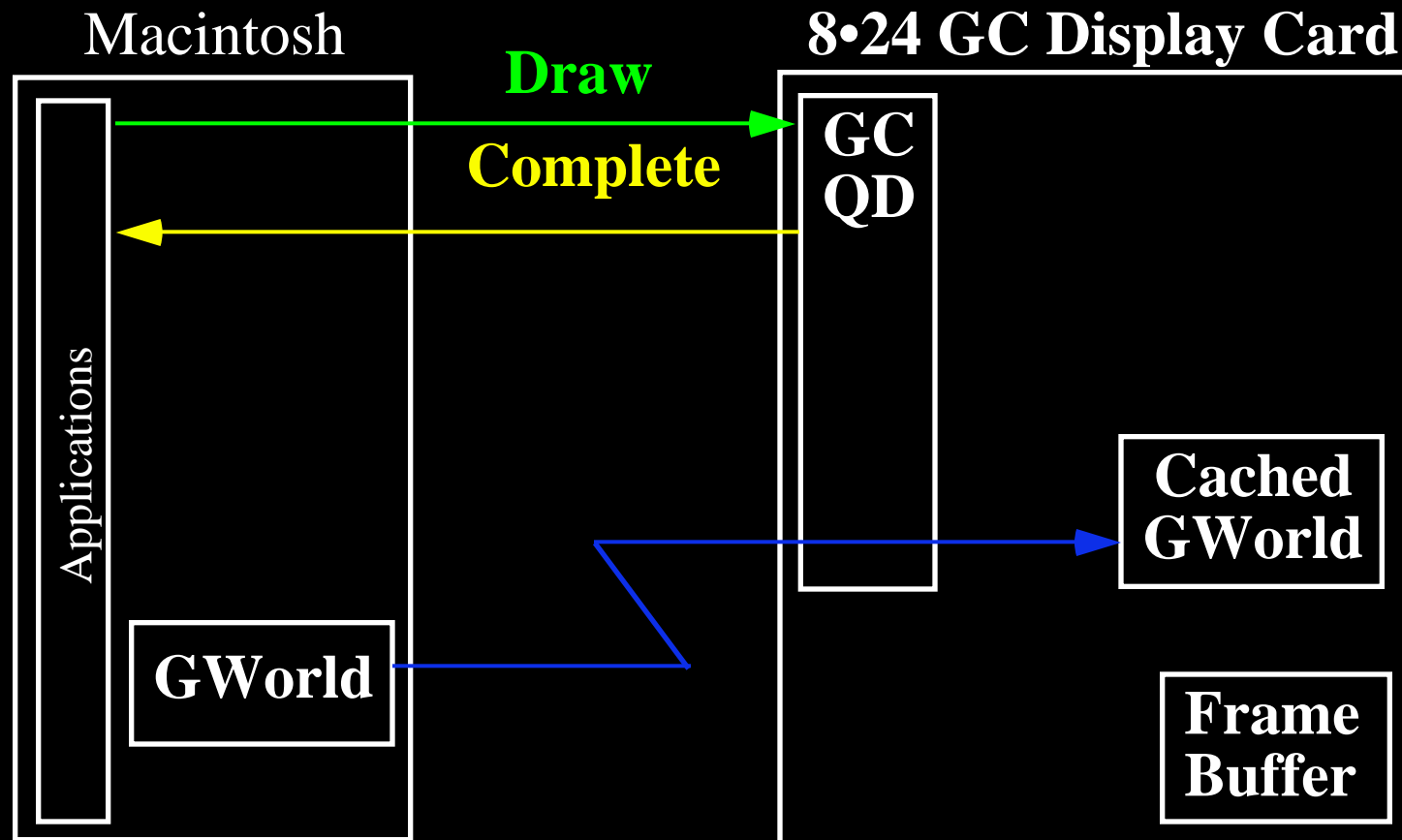
Offscreens: Parallelism - GWorlds



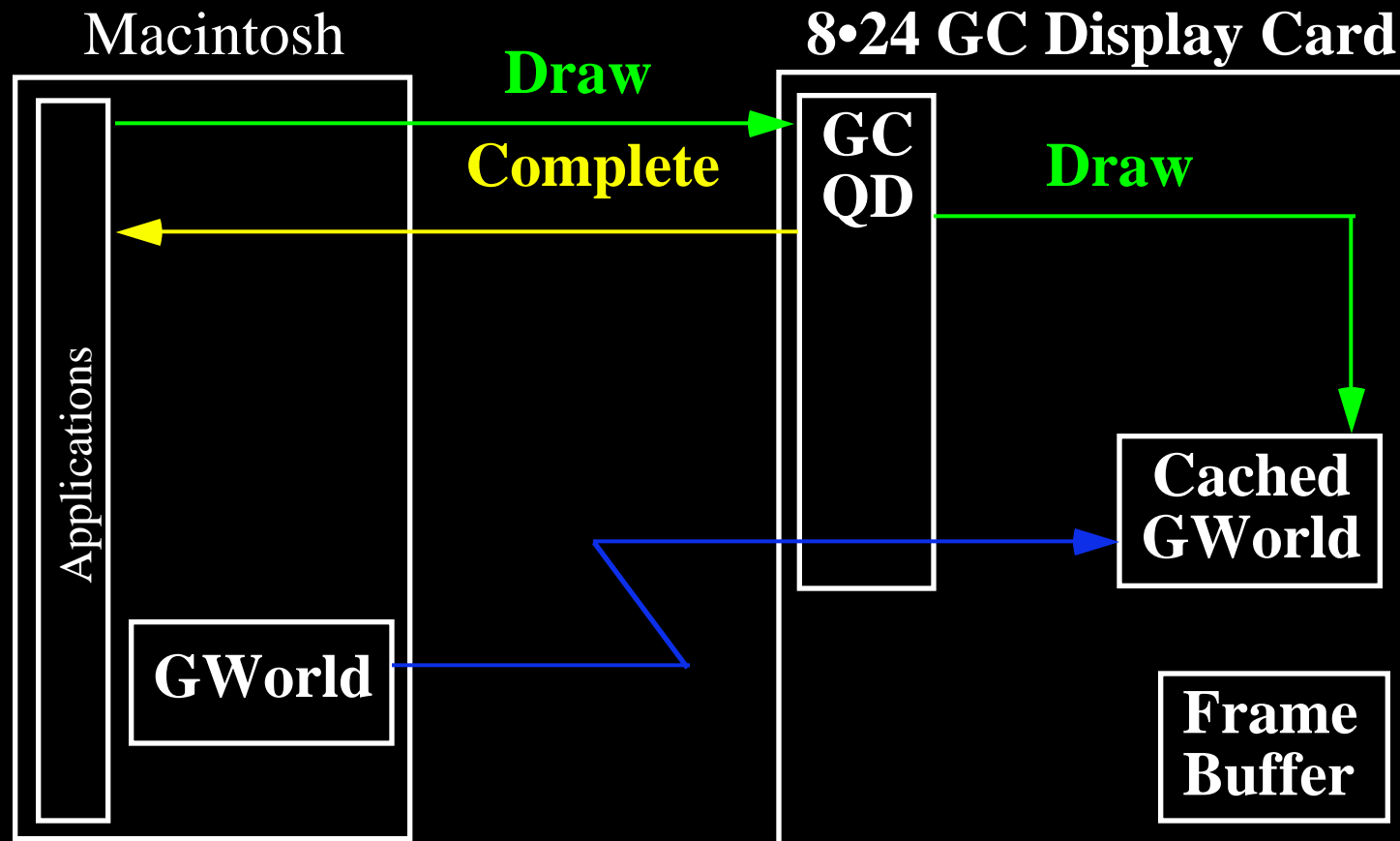
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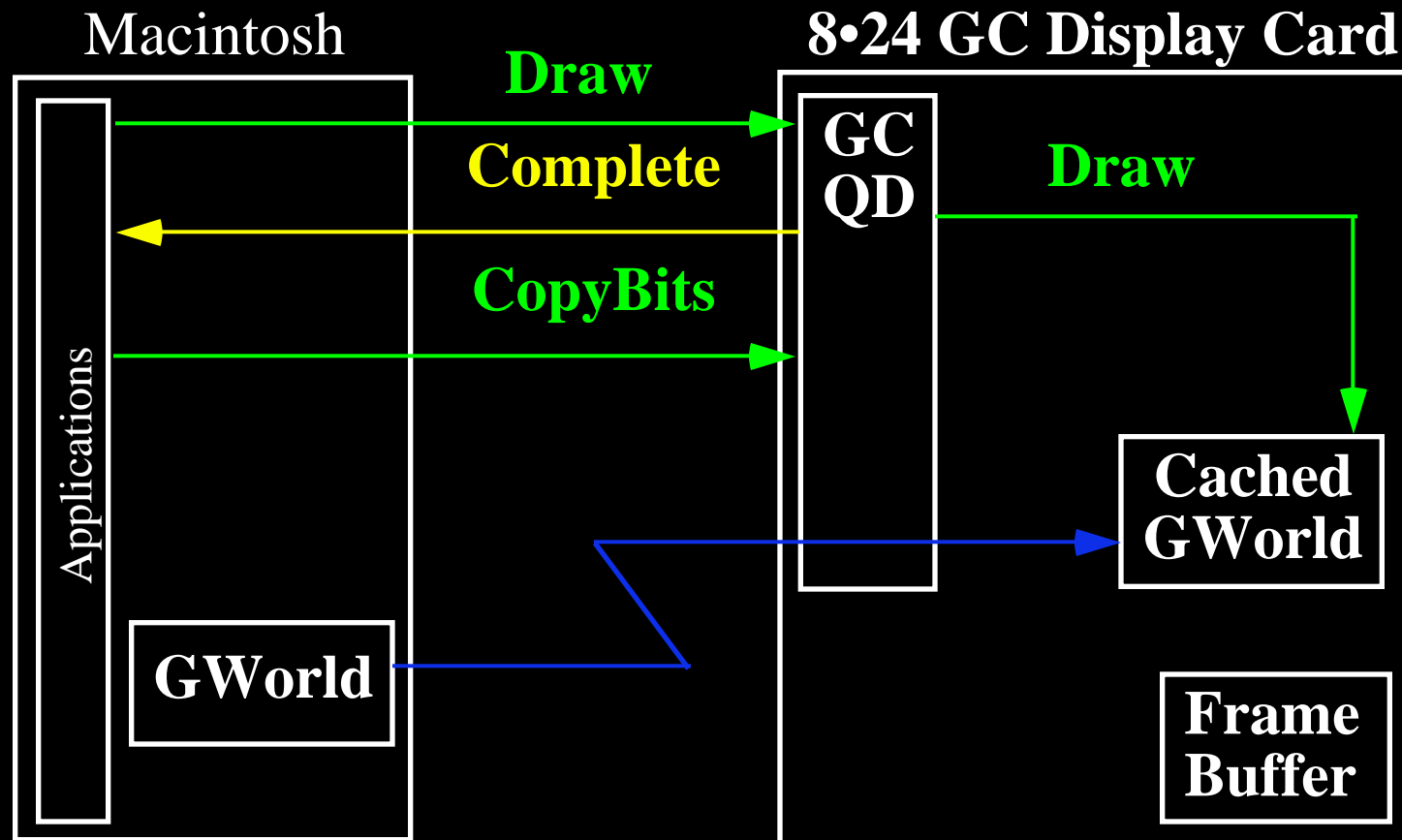
Offscreens: Parallelism - GWorlds



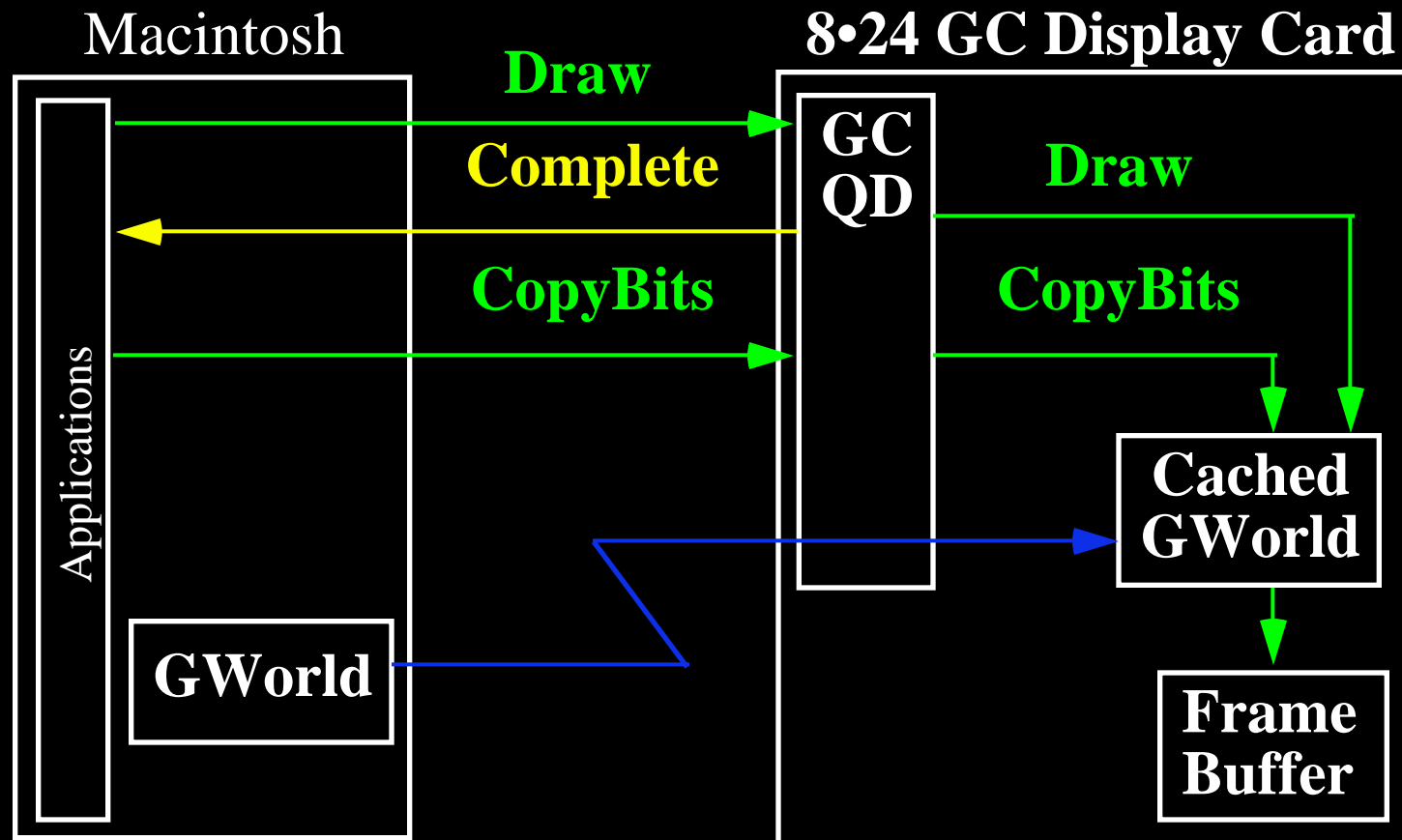
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Offscreens: Parallelism - GWorlds



Inside The 8•24 GC - Optimizing

How To Optimize...

- Think “parallel”
 - Animation using double-buffered GWorlds
 - 32-bit address PixMaps with memory cards



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Inside The 8•24 GC - Optimizing

How To Optimize...

- Think “parallel”
 - Animation using double-buffered GWorlds
 - 32-bit address PixMaps with memory cards
- GetPixBaseAddr call
- QDDone(GrafPort) call



Inside The 8•24 GC

Message to Hardware Developers...

- Implement block transfer
 - Slave mode as a minimum
 - Master mode for intelligent cards



Inside The 8•24 GC

Message to Hardware Developers...

- Implement block transfer
 - Slave mode as a minimum
 - Master mode for intelligent cards
- Especially frame buffers and memory cards

